

Attorney Docket No. 0756-2294

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/832,867

Filed: April 12, 2001

For: LIGHT EMITTING DEVICE

COMPRISING THIN FILM

TRANSISTOR WITH DISTINCT

POSITION OF GATE ELECTRODE

AND IMPURITY REGIONS

) Group Art Unit: 2822

) Examiner: M. Lewis

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Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Examiner's attention is directed to U.S. Patent Application Nos. 09/454,146 and 10/386,257 which are related to the present patent application.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

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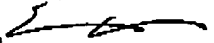
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**FACSIMILE COVER SHEET**

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| To:   | Examiner Lewis               | From:         | Eric J. Robinson |
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| Number of Pages (including cover sheet):  |                              |               |                  |
| <p>RE: Serial No. 09/832,867<br/>Our Reference: 0756-2294</p> <p>Examiner Lewis:</p> <p>As you requested, attached please find a copy of the IDS and Form PTO-1449 filed on December 8, 2003, along with the stamped receipt card evidencing receipt from the Patent Office.</p> <p>Eric Robinson <br/>Reg. No. 38,285</p> |                              |               |                  |

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This will acknowledge receipt of the following:

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In re PATENT application of:

SHUNPEI YAMAZAKI et al

Serial No. 09/832,867

Filed: 04/12/2001

Due Date: 12/01/2003

For: LIGHT EMITTING DEVICE COMPRISING THIN FILM TRANSISTOR WITH DISTINCT POSITION OF GATE ELECTRODE AND IMPURITY REGIONS

Docket: 0756-2294

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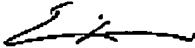
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- 2 -

Application Serial No. 09/832,867  
Attorney Docket No. 0756-2294

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\_\_\_\_\_  
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| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>(use as many sheets as necessary) |                        | Application Number       | 09/832,867               |
|   |                        | Filing Date              | April 12, 2001           |
|   |                        | First Named Inventor     | Shunpei YAMAZAKI, et al. |
|   |                        | Group Art Unit           | 2822                     |
|   |                        | Examiner Name            | Monica Lewis             |
| Sheet 1 of 3  | Attorney Docket Number | 0756-2294                |                          |

| U.S. PATENT DOCUMENTS          |                       |                      |                                   |  |  |   |
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|                                |                       | Number               | Kind Code <sup>2</sup> (if known) |  |  |   |
|                                |                       | 4,963,504            |                                   | Huang  | 10/16/90   |   |
|                                |                       | 5,323,042            |                                   | Matsumoto                                      | 06/21/94   |   |
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| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>(use as many sheets as necessary) |   | Application Number       | 09/832,867               |                        |           |
|   |   | Filing Date              | April 12, 2001           |                        |           |
|   |   | First Named Inventor     | Shunpei YAMAZAKI, et al. |                        |           |
|   |   | Group Art Unit           | 2822                     |                        |           |
|   |   | Examiner Name            | Monica Lewis             |                        |           |
| Sheet   | 2 | of                       | 3                        | Attorney Docket Number | 0756-2294 |

| U.S. PATENT DOCUMENTS          |                       |                      |                                   |   |  |   |
|--------------------------------|-----------------------|----------------------|-----------------------------------|---|--|---|
| Examiner Initials <sup>1</sup> | Cite No. <sup>1</sup> | U.S. Patent Document |                                   | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Paragraphs or Relevant Figures Appear |
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| OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS |                       |   |                |
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|   |                       | US Patent Application Serial No. 10/386,257 including specification, drawings, claims, and filing receipt; filed March 11, 2003; Inventor: Hideto Ohnuma.   |                |
|   |                       | US Patent Application Serial No. 09/454,146 including specification, drawings, claims, and filing receipt; filed December 3, 1999; Inventors: Shunpei YAMAZAKI, et al.  |                |
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|   |   |    |   | Examiner Name            | Monica Lewis             |
| Sheet   | 3 | of | 3 | Attorney Docket Number   | 0756-2294                |

| FOREIGN PATENT DOCUMENTS       |                       |                         |                     |                                   |   |  |   |                |
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|                                |                       | Office <sup>3</sup>     | Number <sup>4</sup> | Kind Code <sup>5</sup> (if known) |   |  |   |                |
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|                                |                       | JP                      | 07-202210           |                                   | Taniguchi                                       | 08/04/95   |   | AB             |
|                                |                       | JP                      | 08-078329           |                                   | Otani   | 03/22/96   |   | AB             |
|                                |                       | JP                      | 08-051207           |                                   | Sato  | 02/20/96   |   | AB             |
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|                                |                       | JP                      | 10-135468           |                                   | Yamazaki et al.                                 | 05/22/98   |   | AB             |

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

| Examiner Initials <sup>1</sup> | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|--------------------------------|-----------------------|---|----------------|
|                                |                       | Tanaka, et al., "Ultrafast Operation of V <sub>th</sub> -Adjusted p <sup>+</sup> -n <sup>+</sup> Double-Gate SOI MOSFET'S," IEEE Electron Device Letters, Vol. 15 (Oct., 1994), No. 10, pp. 386-388.  | ENG            |
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